

Electronic Patent Application Fee Transmittal

Application Number:	10717977			
Filing Date:	20-Nov-2003			
Title of Invention:	Parallel multi-layer printed circuit board having improved interconnection and method for manufacturing the same			
First Named Inventor/Applicant Name:	Jee-Soo Mok			
Filer:	Laura Cruz/Marivick Watson			
Attorney Docket Number:	LEPA122042			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				120